CSCI 5922 - NEURAL NETWORKS AND DEEP LEARNING

DEEP LEARNING SOFTWARE

HISTORY OF FRAMEWORKS

First frameworks to support pure automatic differentiation. SECOND GENERATION			ENERATION
FIRST GENERATION			
		Framework	Year
Framework	Year	Autograd	2015
Torch	2002	` Chainer	2015
Theano	2010	MXNet	2015
Torch7 k	2011	TensorFlow	2016
Theano	2012	Theano	2016
Pylearn2	2013	DvNet	2017
1 Koras	2015	PvTorch	2017
Terchnot	2015	lanite	2018
		JAX	2018
		TensorFlow	2019

Wrappers

First full frameworks to support CUDA/GPUs.

ALL OF THE FRAMEWORKS DISCUSSED SO FAR REQUIRE THE PROGRAMMER TO DEFINE THE COMPUTATIONAL GRAPH PRIOR TO RUNNING IT AND THE GRAPH IS STATIC (IN CHAINER NOMENCLATURE, THESE FRAMEWORKS ARE DEFINE-THEN-RUN).

> THE STATIC GRAPH, DEFINE-THEN-RUN FRAMEWORKS LIMIT THE PROGRAMMER'S ABILITY TO EXPRESS COMPUTATIONS USING THE PROGRAMMING LANGUAGES NATIVE CONSTRUCTS, SUCH AS LOOPS AND CONDITIONALS.

STARTING IN 2015, MANY NEW FRAMEWORKS STARTED TO USE DYNAMIC GRAPHS (IN CHAINER NOMENCLATURE, THESE ARE DEFINE-BY-RUN).

> THIS PARADIGM IS ESSENTIALLY THE SAME AS AUTOMATIC DIFFERENTIATION BY METHOD OVERLOADING, WHICH WE SAW IN <u>AUTOMATIC DIFFERENTIATION IN MACHINE</u> LEARNING: A SURVEY AND WHICH WAS NOT INVENTED BY THE DEEP LEARNING COMMUNITY





Chainer

- First deep learning framework with both
 - Pure AD framework, dynamic graph, define-by-run
 - GPU support
- CuPy
 - Low-level numerical library used by Chainer
 - Near drop-in replacement for NumPy
 - Just for GPUs

- Lots of support for production deployment (e.g. <u>TensorFlow Serving</u>, <u>TensorFlow.js</u>)
- Fairly straightforward to build preprocessing into the computational graph
 - This is super helpful for reproducibility and production use cases. Why?
- Static computational graph
 - Allows graph to be optimized prior to execution (in principle)
 - ▶ In practice, see <u>XLA</u> (Accelerated Linear Algebra), which is just-in-time.
 - When flow control is required, developer needs to become fluent in new API (e.g. tf.cond, tf.while_loop)
 - When print statements are required, the developer needs to use an API

x = tf.Print(x, data=[x.size()], message='Length of vector')

Wanton use of Python context managers (e.g. with tf.variable_scope(...))

DYNET - 2017





PYTORCH - 2017



A SIMPLE UNET IN TWO SLIDES.



WE WON'T REPRODUCE ITS NAMESAKE EXACTLY.

A SIMPLE UNET IN TWO SLIDES.

```
import torch
import torch nn as nn
import torch nn functional as F
class UNetModule(nn.Module):
   def init (self, in channels, out channels, mode=None):
        super(). init ()
       assert mode in ['encoder', 'decoder']
        self.mode = mode
        if self.mode is 'encoder':
            self.conv1 = nn.Conv2d(in channels, out channels, 3,
                                   stride=2, padding=1, bias=False)
        else:
            self.conv1 = nn.Conv2d(in channels, out channels, 3,
                                   stride=1, padding=1, bias=False)
        self.act1 = nn.ReLU(out channels)
        if self.mode == 'decoder':
            self.transposed = nn.ConvTranspose2d(
                out channels, out channels, 3,
                stride=2, padding=1, output padding=1, bias=False)
   def forward(self, input, lateral input=None):
       if self.mode == 'decoder' and lateral input is not None:
            input = torch.cat((input, lateral input), dim=1)
       output = self.conv1(input)
        output = self.act1(output)
       if self.mode == 'decoder':
            output = self.transposed(output)
        return output
```

```
class UNet(nn.Module):
                                  def init (self, n classes, n input channels=3):
                                      super(). init ()
                                      self.n classes = n classes
                                      self.encoder1 = UNetModule(n input channels, 16, mode='encoder')
                                      self.encoder2 = UNetModule(16, 16, mode='encoder')
                                      self.encoder3 = UNetModule(16, 16, mode='encoder')
                                      self.encoder4 = UNetModule(16, 16, mode='encoder')
                                      self.decoder4 = UNetModule(16, 16, mode='decoder')
A SIMPLE UNET IN TWO SLIDES.
                                      self.decoder3 = UNetModule(32, 16, mode='decoder')
                                      self.decoder2 = UNetModule(32, 16, mode='decoder')
                                      self.decoder1 = UNetModule(32, 16, mode='decoder')
                                      self.classifier = nn.Conv2d(16, n classes, 1, padding=0, bias=False)
                                  def forward(self, input):
                                      encoder1 output = self.encoder1(input)
                                      encoder2 output = self.encoder2(encoder1 output)
                                      encoder3 output = self.encoder3(encoder2 output)
                                      encoder4 output = self.encoder4(encoder3 output)
                                      decoder4 output = self.decoder4(encoder4 output)
                                      decoder3 output = self.decoder3(decoder4 output, encoder3 output)
                                      decoder2 output = self.decoder2(decoder3 output, encoder2 output)
                                      decoder1 output = self.decoder1(decoder2 output, encoder1 output)
                                      output = self.classifier(decoder1 output)
                                      return output
```

RECALL THAT WHEN A TENSOR TAKES MULTIPLE PATHS THROUGH A NETWORK (I.E. THERE'S A FORK IN THE ROAD, AND IT TAKES BOTH), THERE ARE — DURING THE BACKWARD PASS — MULTIPLE GRADIENTS, ONE FOR EACH PATH THE TENSOR TOOK.

ALL FRAMEWORKS SUM THESE GRADIENTS BY DEFAULT. GETTING ACCESS TO THE PRE-SUMMED GRADIENTS CAN BE TRICKY.

THE NEXT THREE SLIDES SHOW ONE WAY TO DO THIS.

PYTORCH SUPPORTS HOOKS ON BOTH MODULES AND TENSORS. DEFINE FUNCTIONS TO PRINT THE GRADIENTS. #!/usr/bin/env python

coding: utf-8

from functools import partial

import torch
import torch.nn as nn

def print_tensor_grad(grad, name=None, value=None):
 print(name, 'value', value, 'grad', grad)

def print_module_grad(module, grad_input, grad_out, name=None):
 print(name, grad_input)

NOW DEFINE THE NETWORK ITSELF. THE NETWORK MUST INHERIT FROM TORCH.NN.MODULE AND MUST CALL THE SUPERCLASS'S INITIALIZER BEFORE ASSIGNING TO SELF IN ITS OWN INITIALIZER. WHY?

A MODULE IS A CONTAINER. FOR IT TO KNOW WHICH OF ITS PROPERTIES ARE PARAMETERS, THE SUPERCLASS OVERRIDES ______SETATTR____, SO WHEN YOU WRITE SELF.LAYER1 = NN.LINEAR(...), IT CAN REGISTER SELF.LAYER1 AS A CHILD MODULE AND AUTOMATICALLY UPDATE ITS PARAMETERS DURING TRAINING.

```
class Network(nn.Module):
    def __init__(self, n_in=2, n_out=2):
        super().__init__()
        self.layer1 = nn.Linear(n_in, n_out, bias=False)
        self.layer2 = nn.Linear(n_out, n_out, bias=False)
        self.layer3 = nn.Linear(n_out*2, 1, bias=False)
        self.fun = nn.LeakyReLU(negative slope=1.0)
```

TO GET ACCESS TO THE PRE-SUMMED GRADIENTS, WE'LL ADD A LEAKY RELU WITH A NEGATIVE SLOPE OF 1 TO THE COMPUTATIONAL GRAPH, AND ADD A HOOK TO IT. WHY?



return {

```
'out1': out1,
'path1': path1,
'path2': path2,
'y': out3
```

WHAT IS GOING ON HERE? HOW DOES THIS ALLOW US TO GET ACCESS TO THE PRE-SUMMED GRADIENTS OF LAYER1?

NOW RUN IT!

```
if __name__ == '__main__':
    torch.manual_seed(17)
    network = Network()
    x = torch.ones(1, 2)
    out = network(x)
    out['y'].backward()
    # Verify that the gradient of the output of the first layer is the
    # same as the sum of the two paths taken by that output.
    print('out1', out['out1'].grad)
    print('path1', out['path1'].grad)
    print('path2', out['path2'].grad)
    assert torch.all(
        out['out1'].grad == out['path1'].grad + out['path2'].grad)
```



DEFINING YOUR OWN PROCESS FUNCTION.

```
def train_and_store_loss(engine, batch):
    # The process function gets called on each iteration.
    inputs, targets = batch
    optimizer.zero_grad()
    outputs = model(inputs)
    loss = loss_fn(outputs, targets)
    loss.backward()
    optimizer.step()
    return loss.item()

engine = Engine(train_and_store_loss)

@engine.on(Events.COMPLETED)
```

```
def cleanup(engine):
    print('Done training on epoch {:04d}'.format(engine.state.epoch))
```

```
# Can also add handlers via a method.
engine.add event handler(Events.COMPLETED, cleanup)
```

```
engine.run(data_loader)
```

EVENTS IN IGNITE

- Events.STARTED
- Events.COMPLETED
- Events.EPOCH_STARTED
- Events.EPOCH_COMPLETED
- Events.ITERATION_STARTED
- Events.ITERATION_COMPLETED
- Events.EXCEPTION_RAISED

- NumPy drop-in replacement with just-in-time compilation
- CPU or GPU
- Automatic vectorization
- Uses TensorFlow's XLA backend to compile quickly for GPU

Super important changes

- Eager mode will be the default
 - Graphs will be dynamic
 - Stack traces will still be ugly
- Keras will be the default way of using the API
 - TensorFlow will look more like Chainer and PyTorch

- Mobile deployment environments
 - Mobile GPUs
 - NVIDIA Jetson TX1, TX2, Xavier AGX
 - Low-power envelope, single GPU, shared memory devices for resource-constrained deployments
 - Jetson TX* have shared memory between CPU and GPU

CONVOLUTIONAL NETWORKS ARE MOVING TO THE EDGE

NVIDIA DRIVE PEGASUS IS DESIGNED FOR ROBOTAXIS.



- Mobile deployment environments
 - FPGAs see talk by Phil James-Roxby this semester
- Mobile phones
 - iOS: Metal Performance Shaders
 - Android and iOS: TensorFlow Lite
 - ONNX (also for general interchange/optimization)

- Allows programming of convnets on platforms that support Apple's Metal API
 - Swift and Objective-C (iOS), possibly C++ on Mac OS
 - Support for constructing multi-layer neural networks, as well as convolutional or recurrent ones
 - Some support for training, but appeal is inference significant speed-ups reported using MPS

TENSORFLOW LITE

- TensorFlow support for GPUs on
 - iOS devices is based on Metal Performance Shaders
 - Android devices is based on OpenGL Compute Shaders
- The GPU Delegate
 - Prunes unnecessary operations
 - Replaces some operations with faster versions
 - Fuses some sequences of operations (e.g. fusing batch norm affine weights into convolution)
 - Falls back to the CPU for operations that are not implemented for the GPU
- Pre-trained models available
 - MobileNet v1 for image classification, PoseNet for pose estimation, DeepLab segmentation, MobileNet SSD object detection